FIG. 1 PRIOR ART

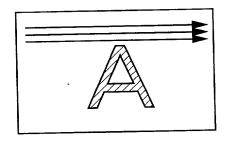


FIG. 2 PRIOR ART

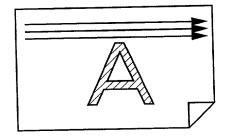


FIG. 3 PRIOR ART

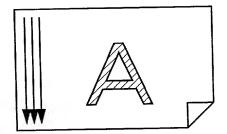


FIG. 4

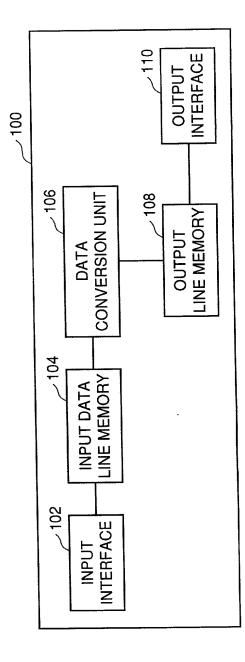


FIG. 5

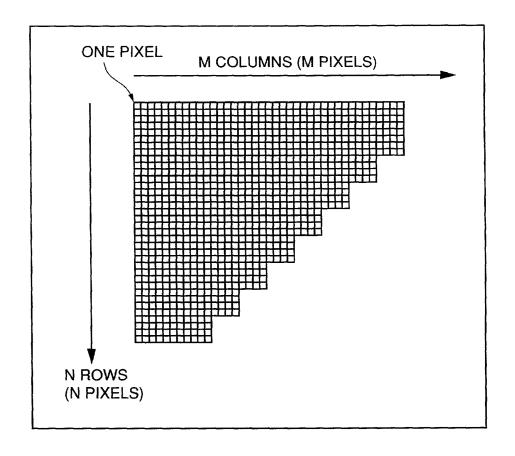
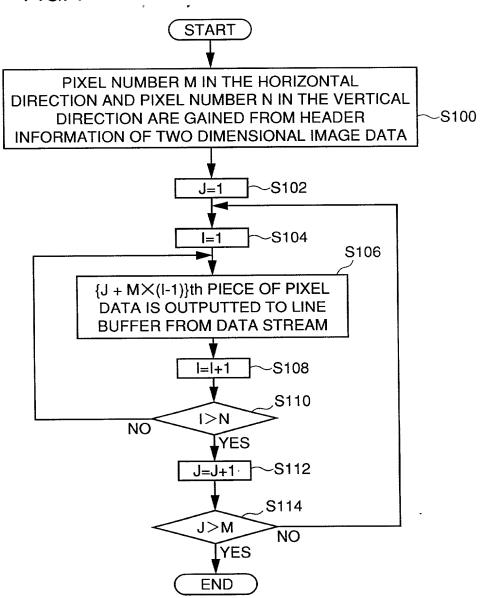
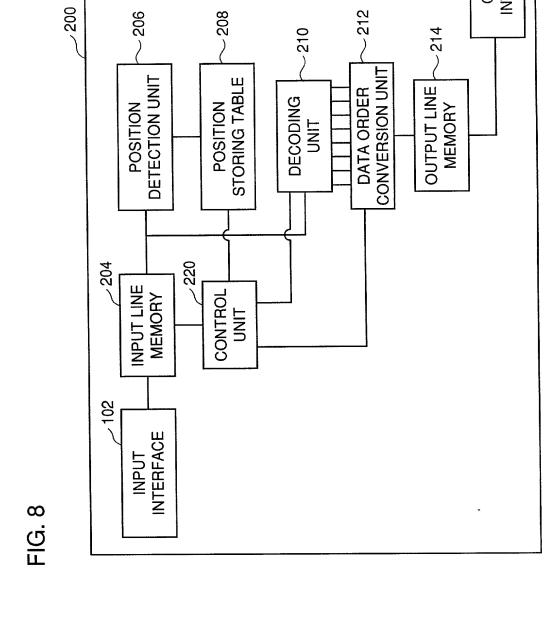


FIG. 6

	DATA COMPLETION FLAG		
	PIXEL DATA 4	ROW	MBER DATA
•	PIXEL DATA 3	COLUMN	NUMBER DATA   NUMBER DATA
	1 PIXEL DATA 2	PIXEL	NUMBER DATA
	PIXEL DATA	ENCEMENT	16
	HEADER	DATA COMMENCEMENT	FLAG

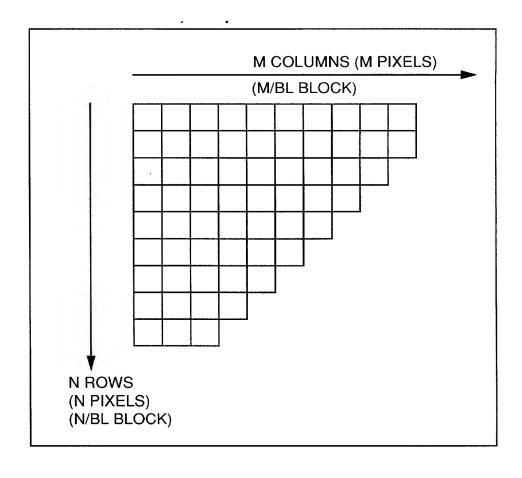
FIG. 7

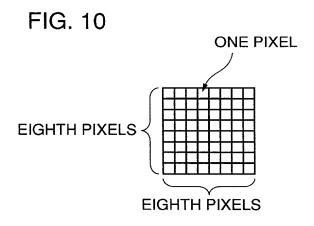




OUTPUT INTERFACE

FIG. 9





FG. 11

DATA COMPLETION FLAG	UMBER DATA
BLOCK 4	BLOCK PIXEL NUMBER DATA
(3	ROW NUMBER DATA
BLOCK 3	COLUMN ROW NUMBER DATA
BLOCK 2	COMPRESSION COLUMN TYPE NUMBER DA
BLOCK 1	DATA COMMENCEMENT FLAG
HEADER	DATA COMIV

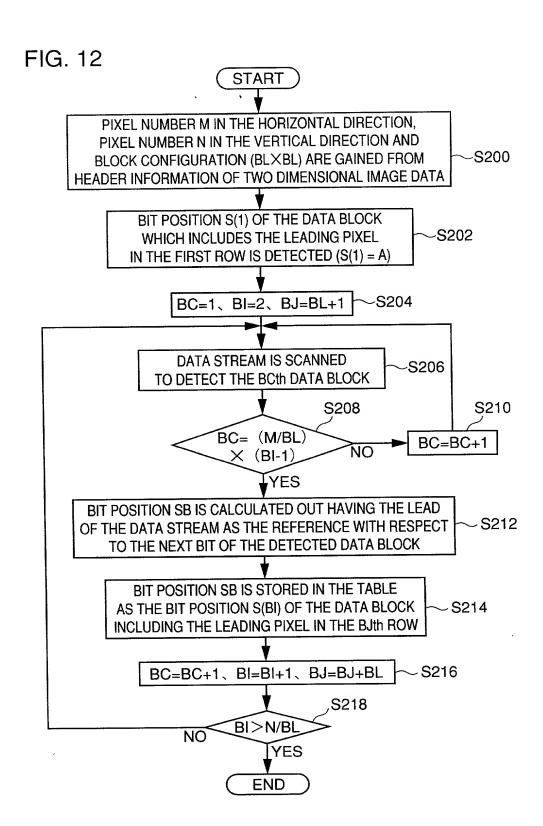
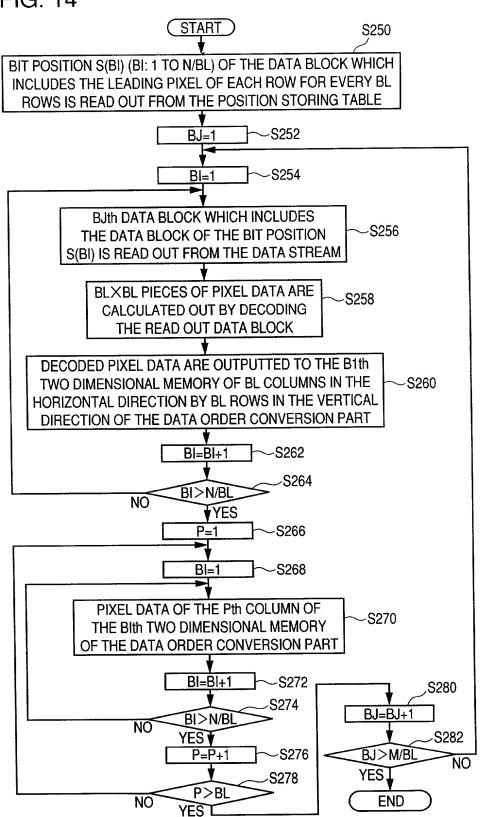


FIG. 13

NUMBER BI	NUMBER OF ROWS BJ (BL = 8) (N = 320)	POSITION INFORMATION BIT POSITION OF THE DATA BLOCK WHICH INCLUDES THE LEADING PIXEL IN THE BJth ROW (LEADING REFERENCE OF THE DATA STREAM)	
1	1	1 S(1) = Ath BIT	
2	9 S(2) = Bth BIT		
3	17	S(3) = Cth BIT	
4	4 25 S(4) = Dth BIT		
	•••	•••	
BI	(BI-1)×8+1	S(BI) = Xth BIT	
•••	•••	•••	
40	313	S(40) = Yth BIT	

FIG. 14





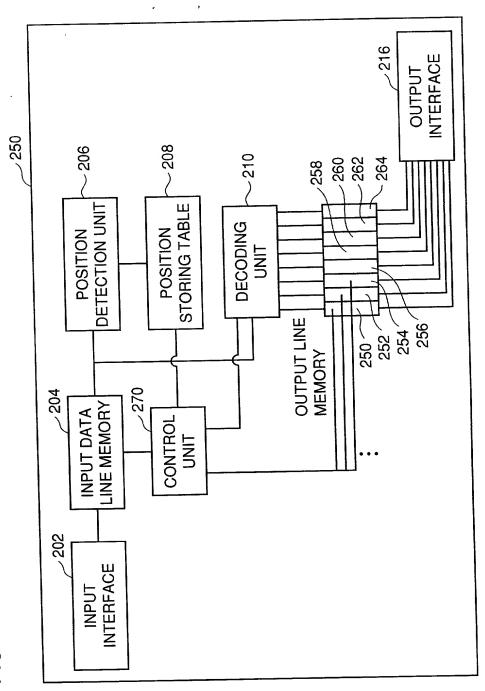


FIG. 16

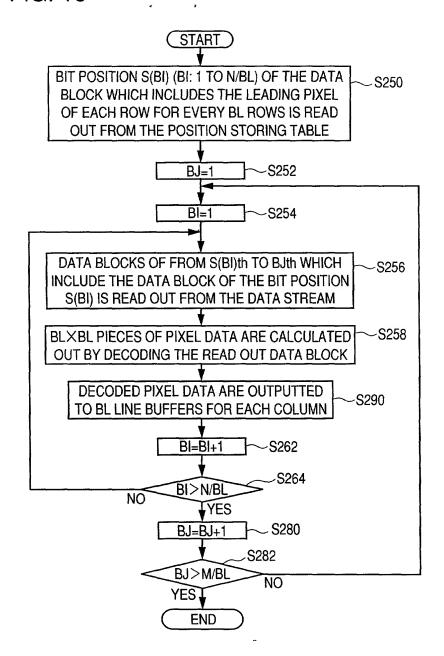
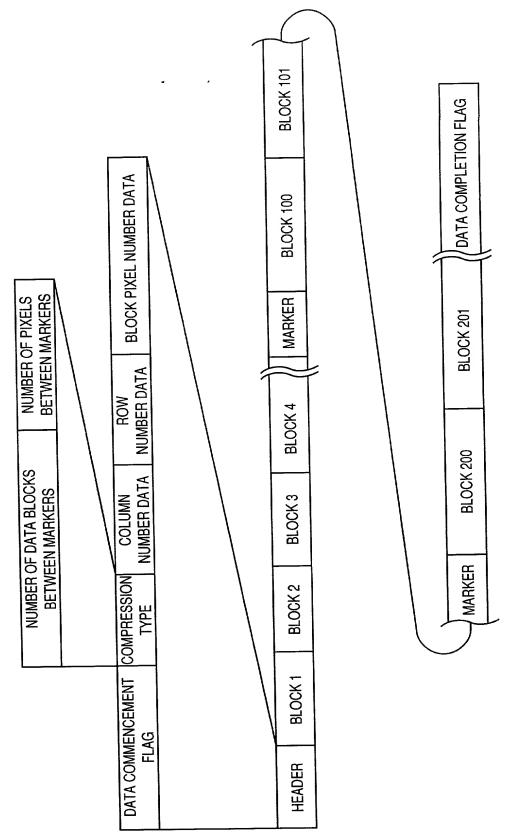
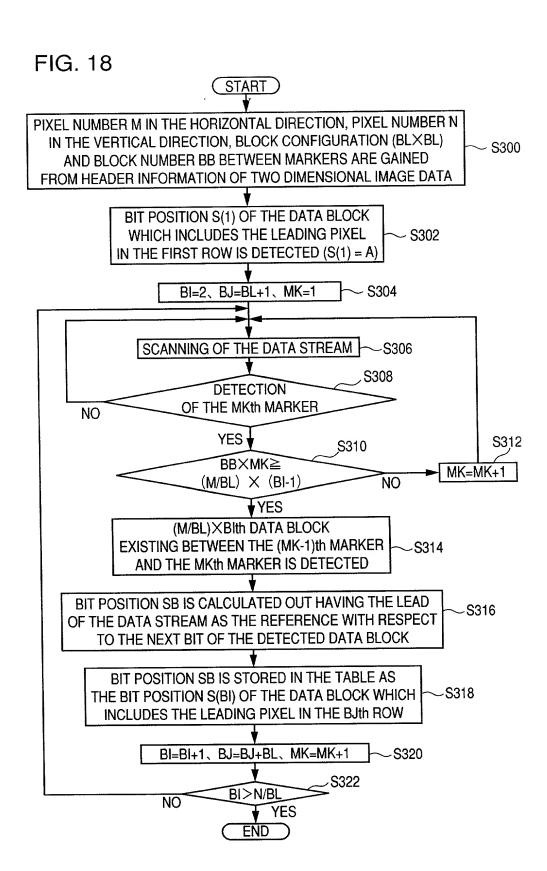


FIG. 17





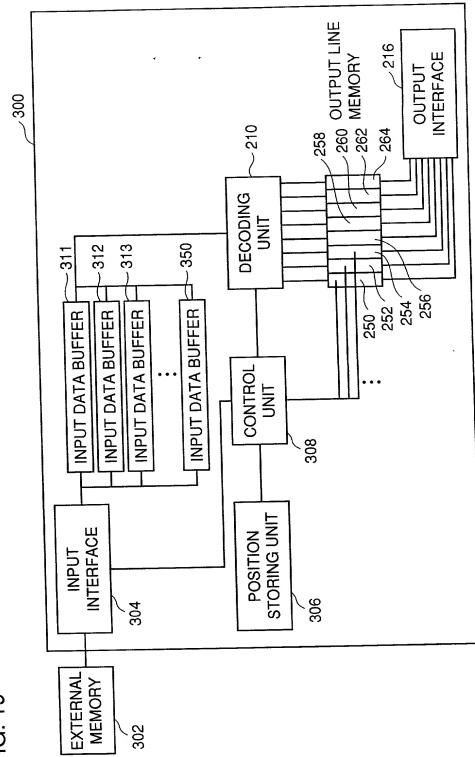


FIG. 19

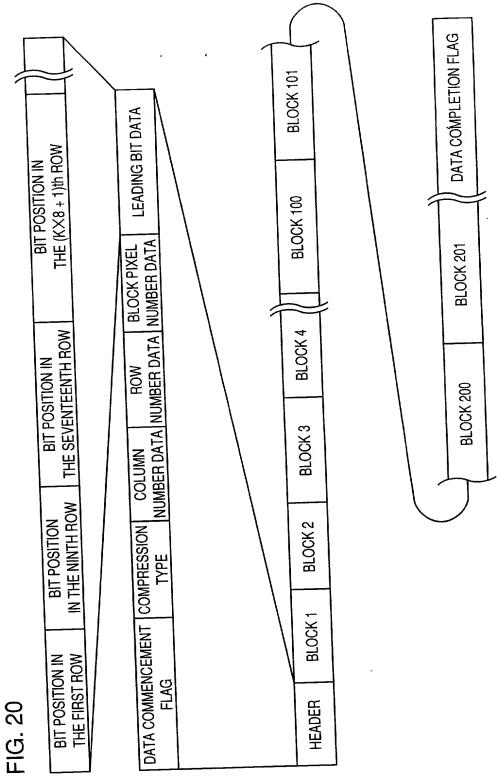


FIG. 21

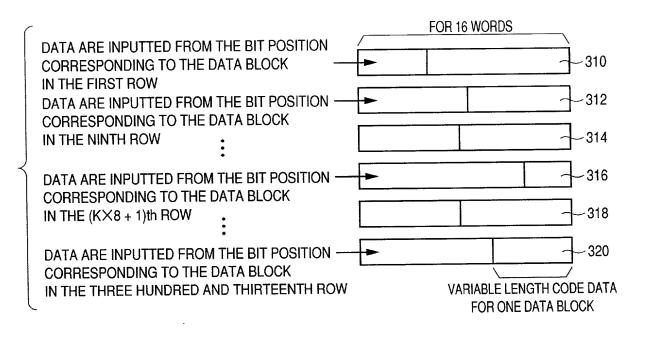


FIG. 22

